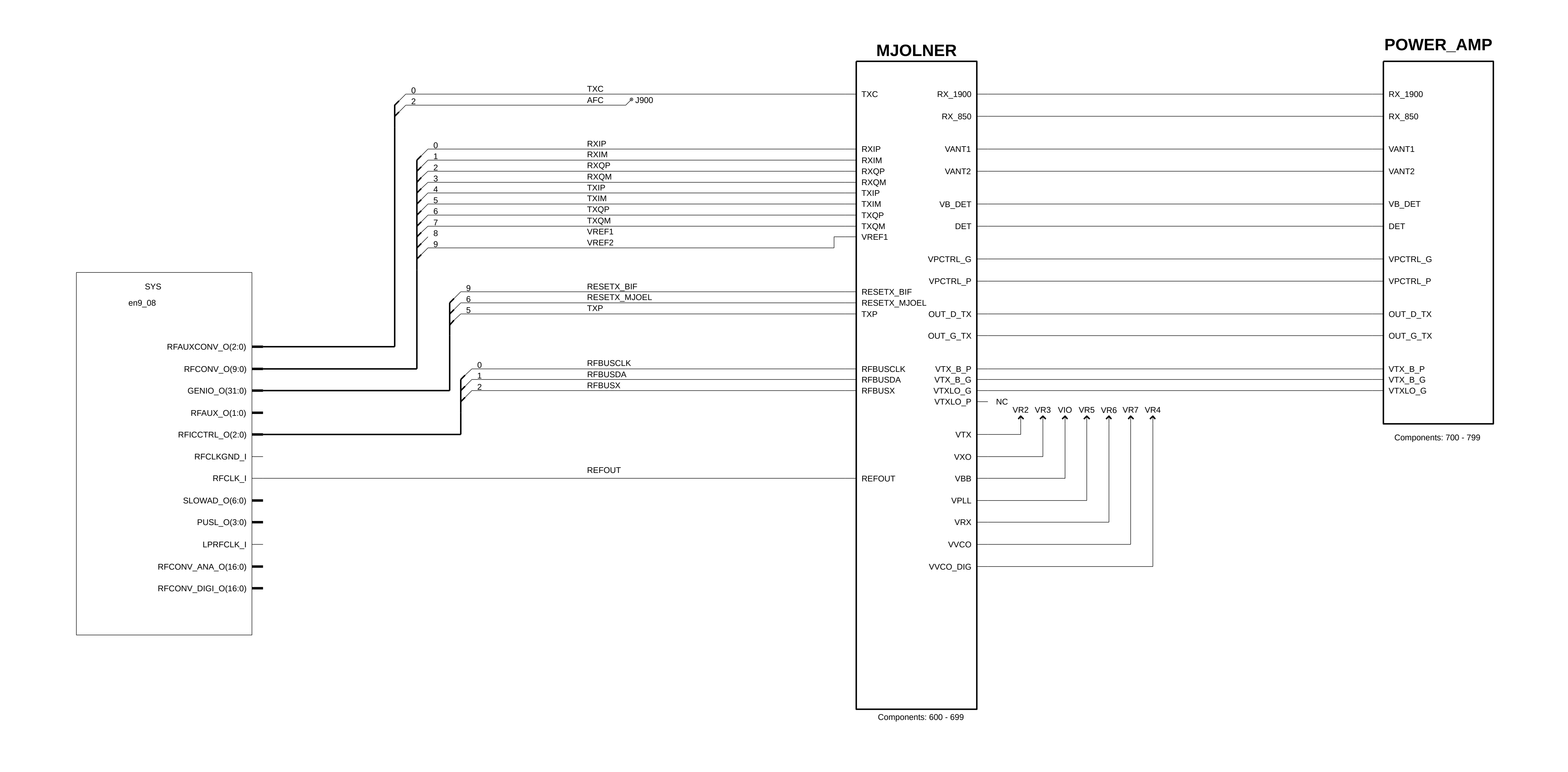
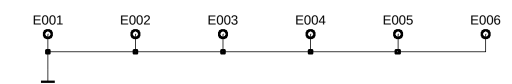
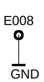
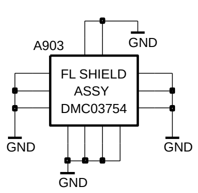
Top-level Schematic1

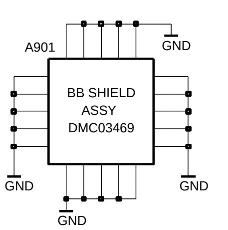


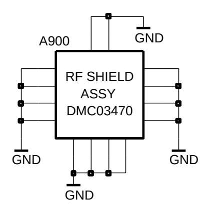
Shield Sheet

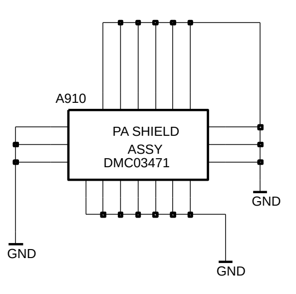




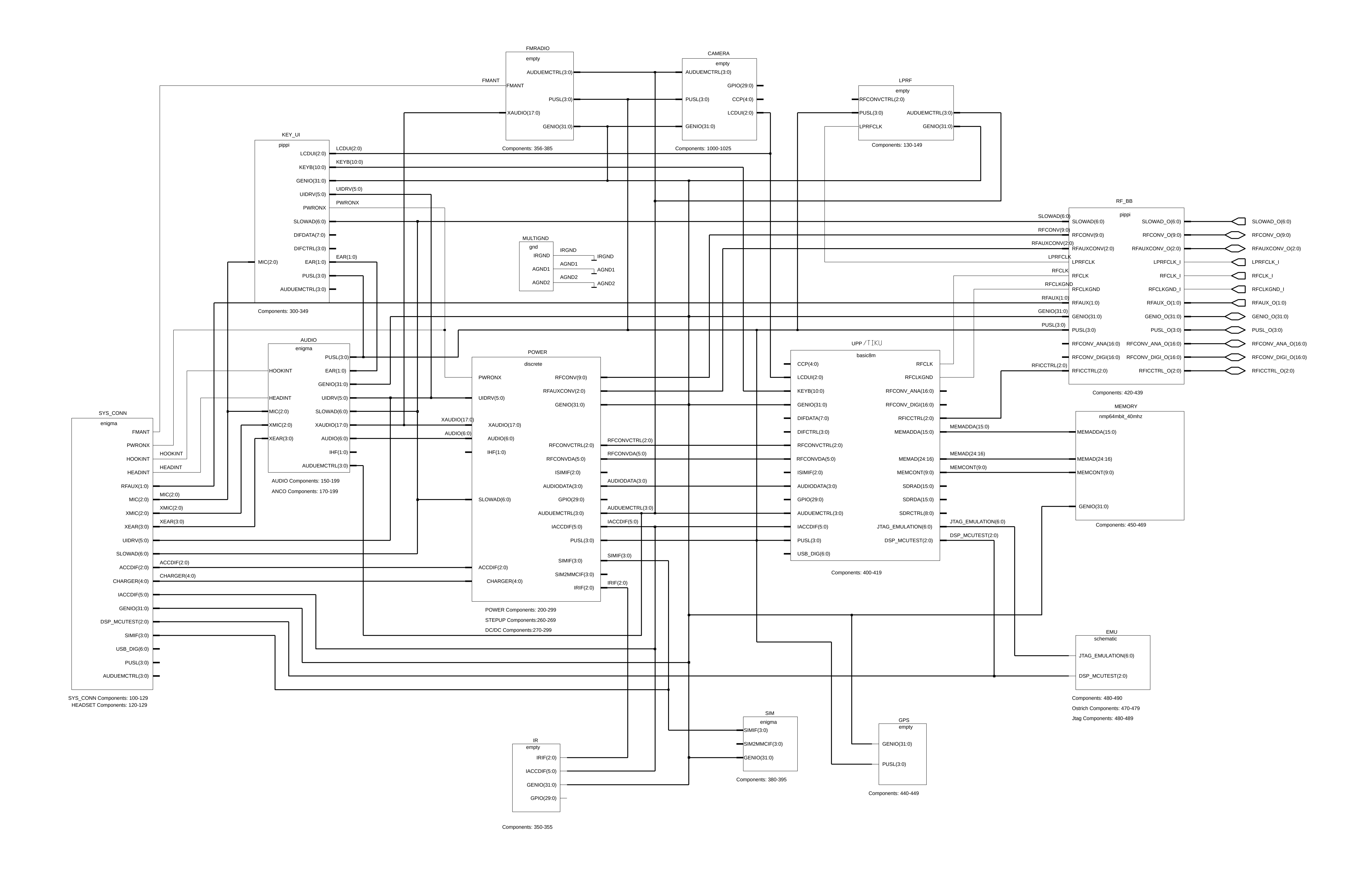




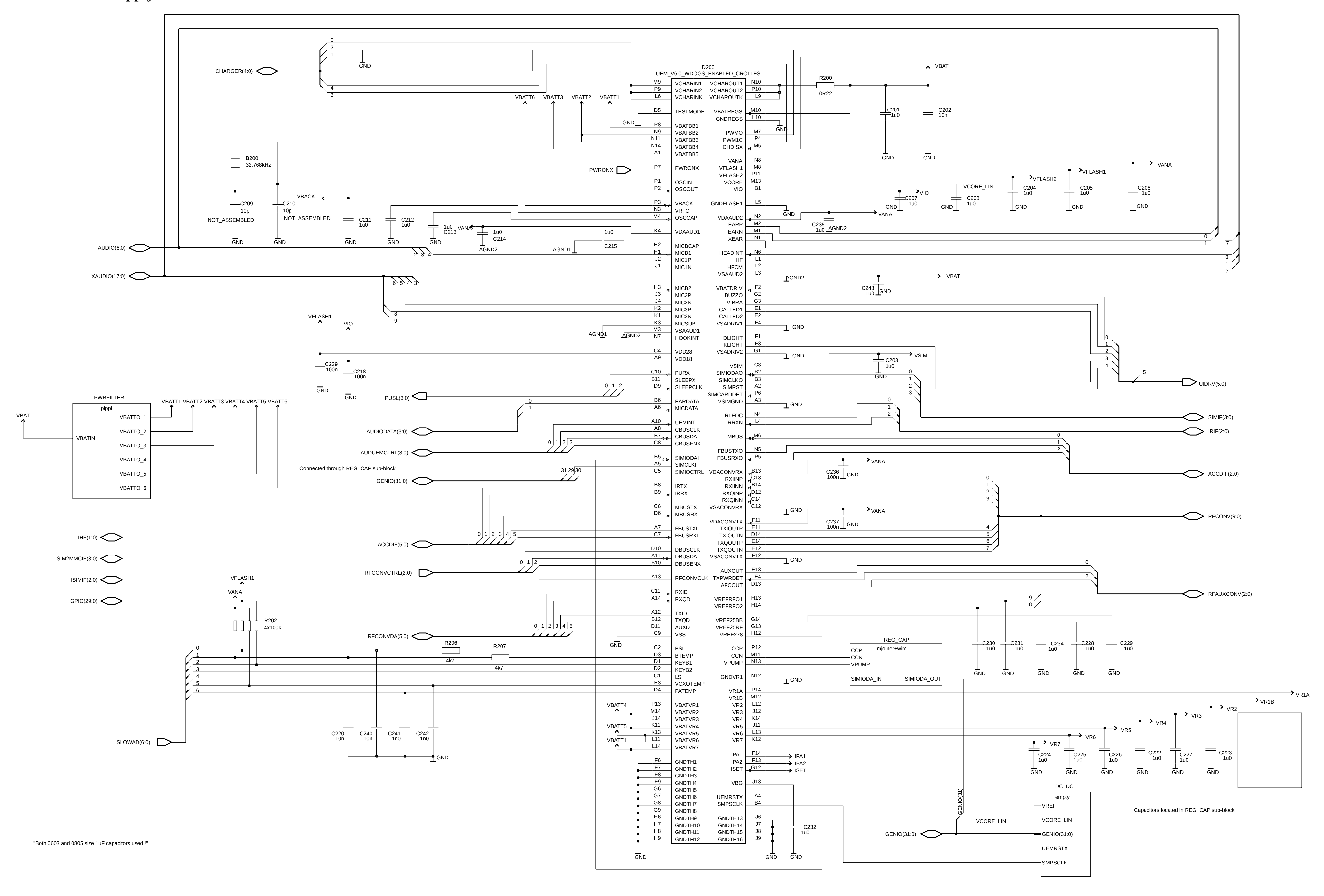




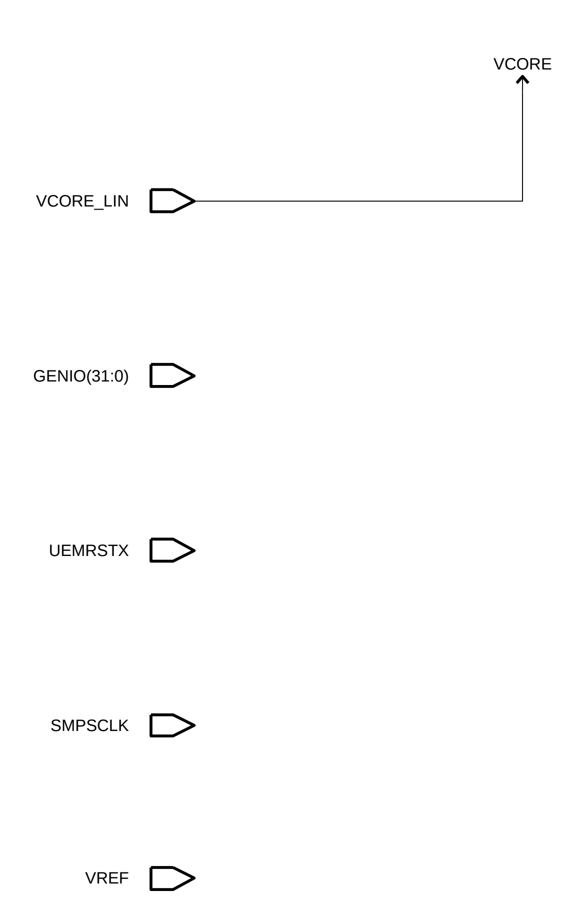
DCT4 Common Baseband



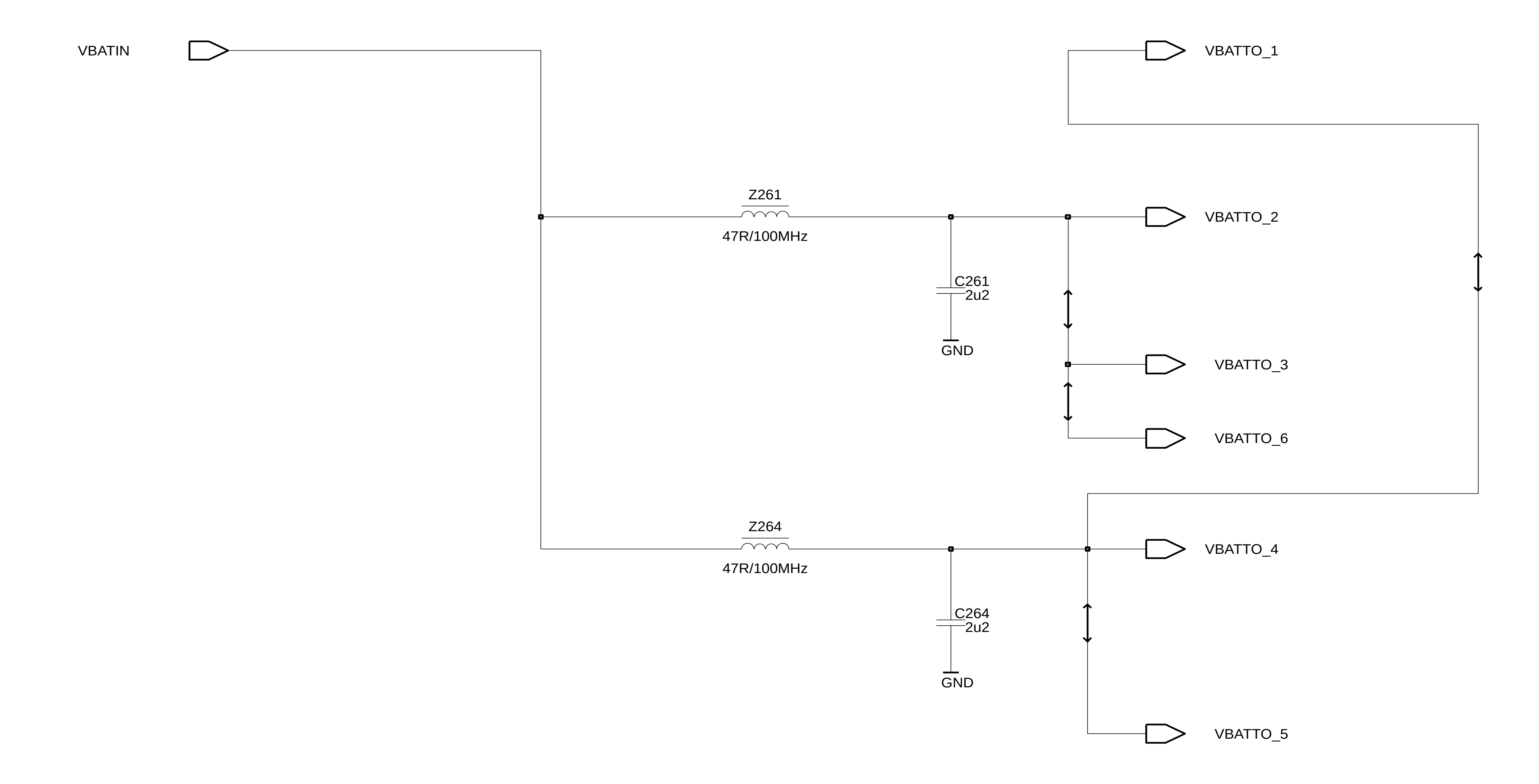
Discrete Power Supply



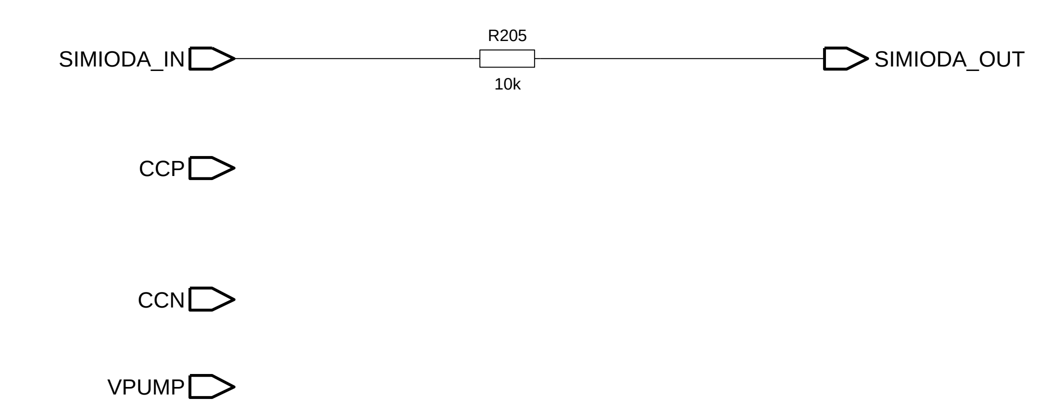
DC/DC Converter



DCT4 Power Supply



DCT4 Common Baseband



System Connector

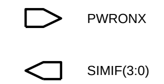
For After Sales

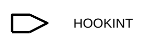


UIDRV(5:0)

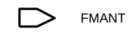


















R123 100k HEADINT

Z102 EXC24CB102U

XEAR(3:0) "XEARN" "XEARP"

XMIC(2:0) "XMICN" "XMICP"

X102 HEADINT INT XEARN XEARP XMICN XMICP DC_CRL DC- DC+ MIC- MIC+

Z101 EXC24CB102U

PRODTP7 C105 10n NOT_ASSEMBLED C106 10n NOT_ASSEMBLED C107 10n not_assembled C108 10n not_assembled

C121 100n C122 100n NOT_ASSEMBLED C123 10n NOT_ASSEMBLED C124 10n

On surface layer R199 0R not_assembled Z105 R198 0R not_assembled VCHAR GND

F100 1.5A L100 42R/100MHz C100 1n0 V101 RB160M-30 V100 1PMT16AT3 GND CHARGER(4:0)

Z100 EXC24CB102U C103 1n0 "MICN" "MICP" MIC(2:0) C101 10n C102 10n GND

PRODTP6 J100 PRODTP7 PRODTP2 PRODTP3 E100 E101 E102 E103 E107 GND

R108 1 /4 100R R109 1k0 C104 10n not_assembled DSP_MCUTEST(2:0)

R108 2 /4 100R R108 3 /4 100R R108 4 /4 100R R107 220k GND

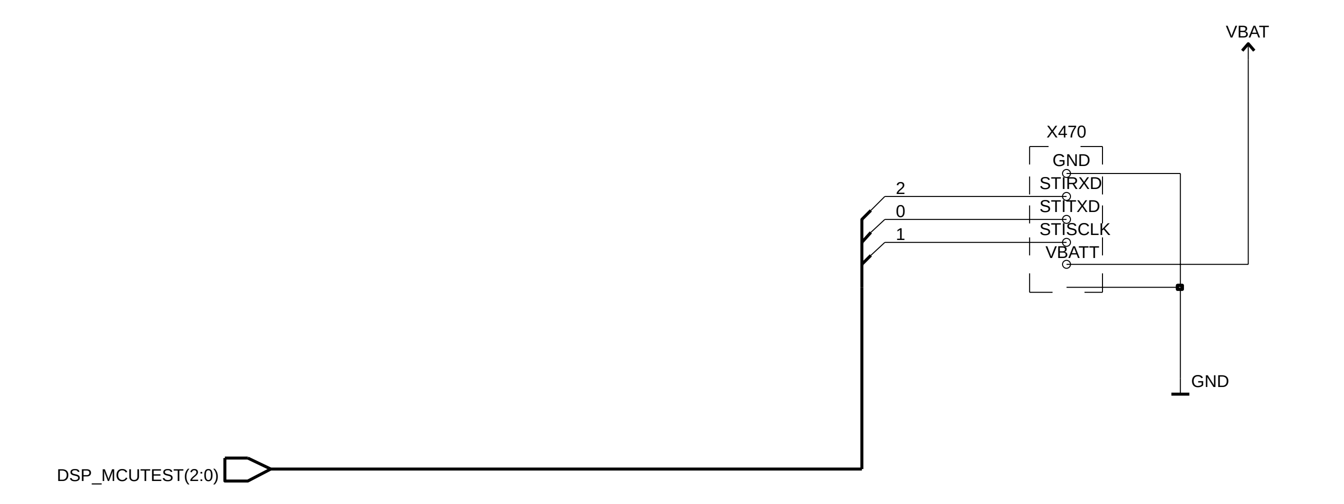
ACCDIF(2:0)

Test and Emulator Interface

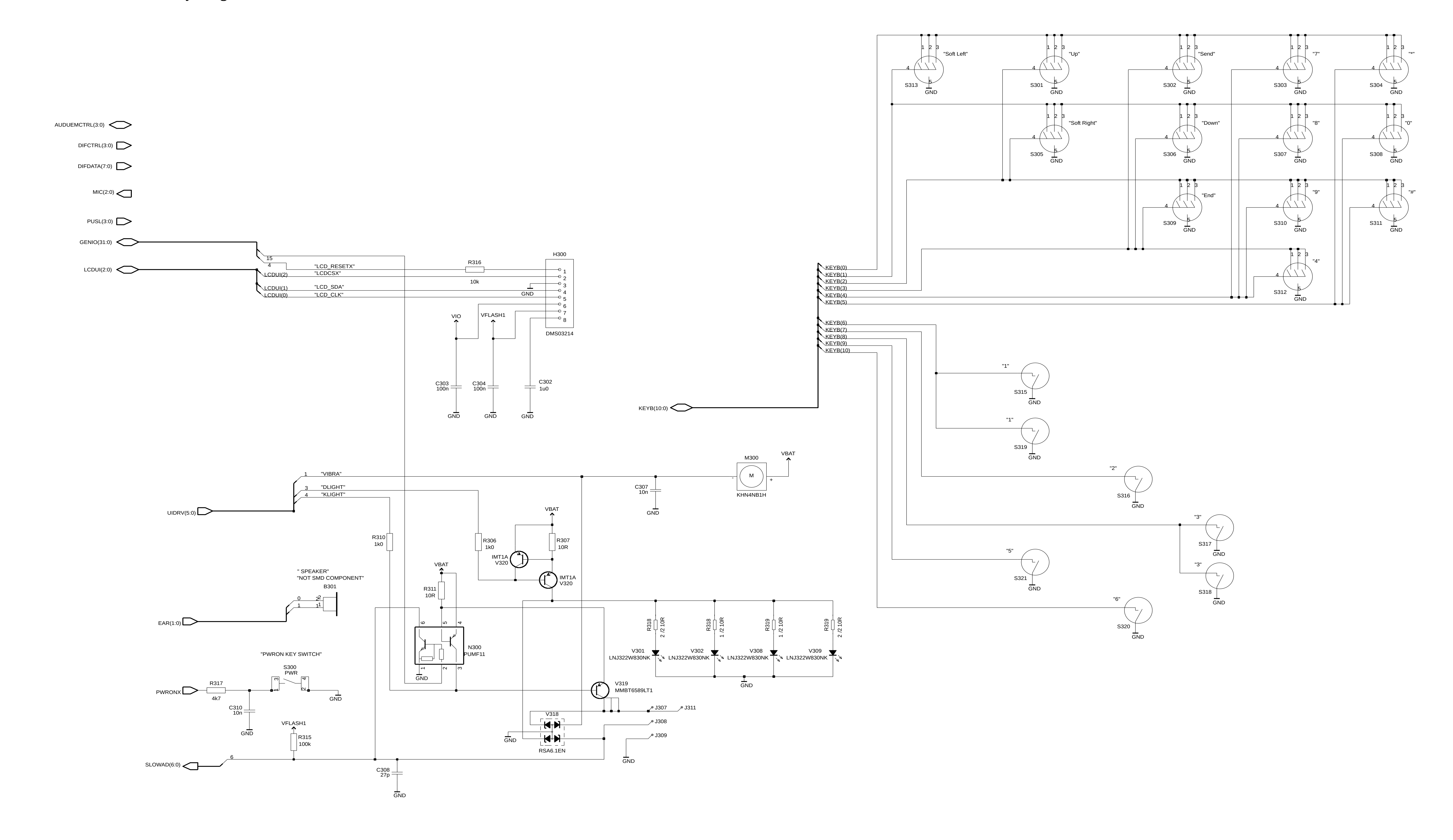




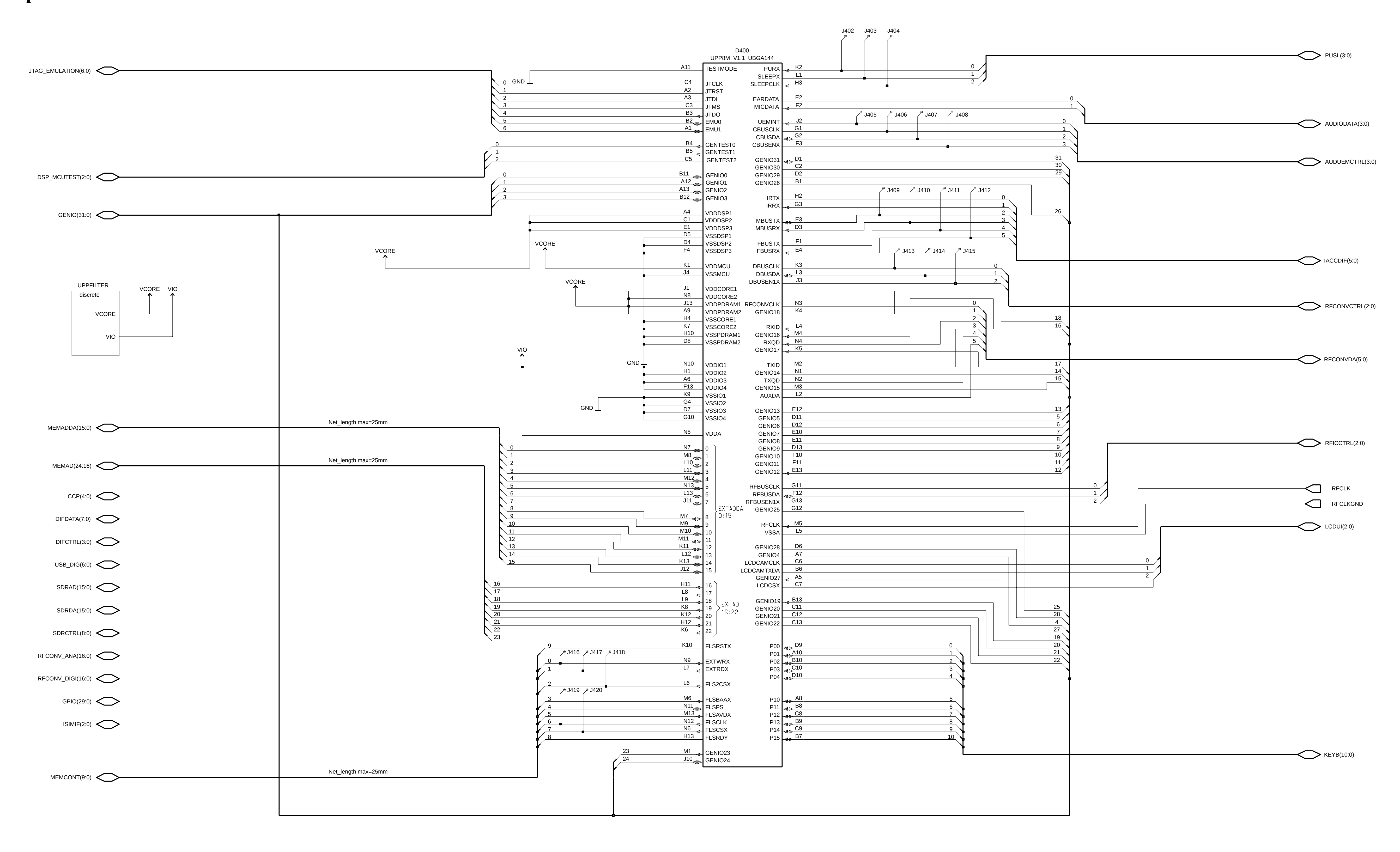
Connector-based Ostrich Test Interface



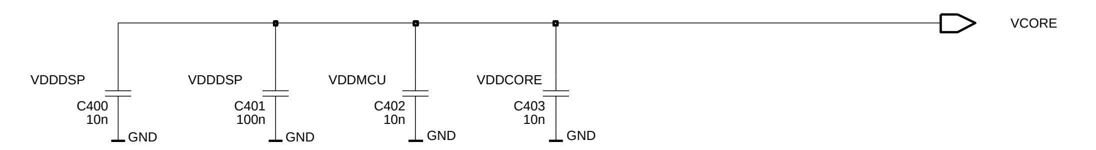
User Interface Clover Key Targets

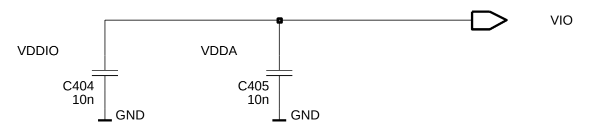


UPP 8M Implementation

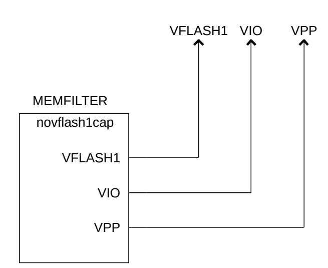


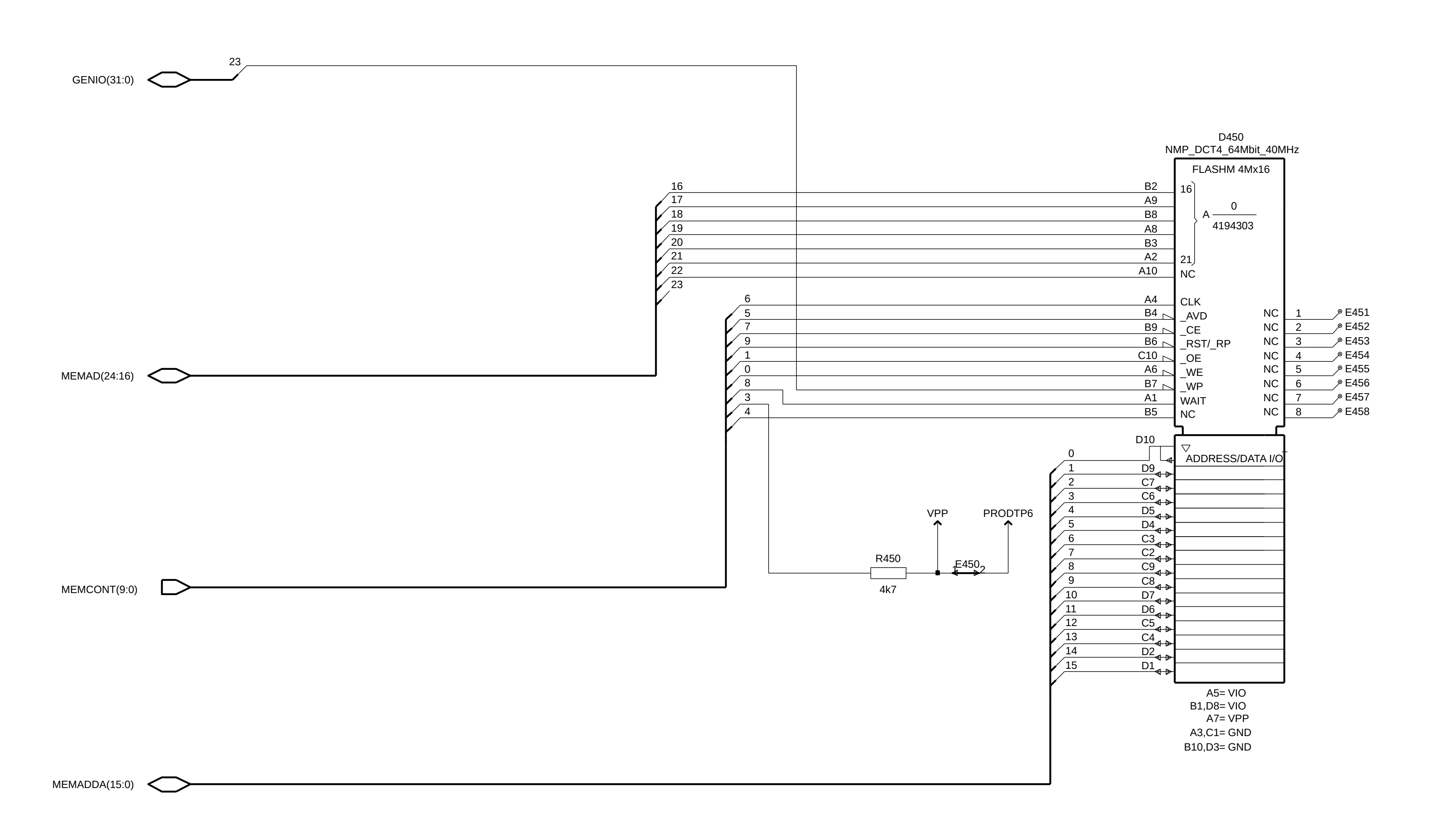
Discrete Decoupling Capacitors for UPP



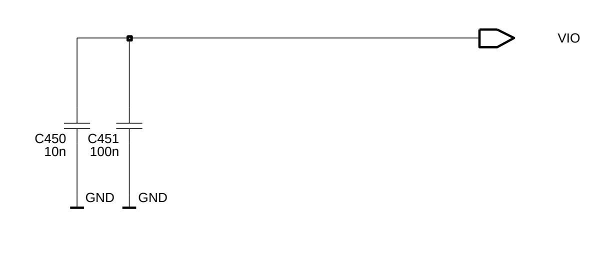


Flash Memory 64 Mbit



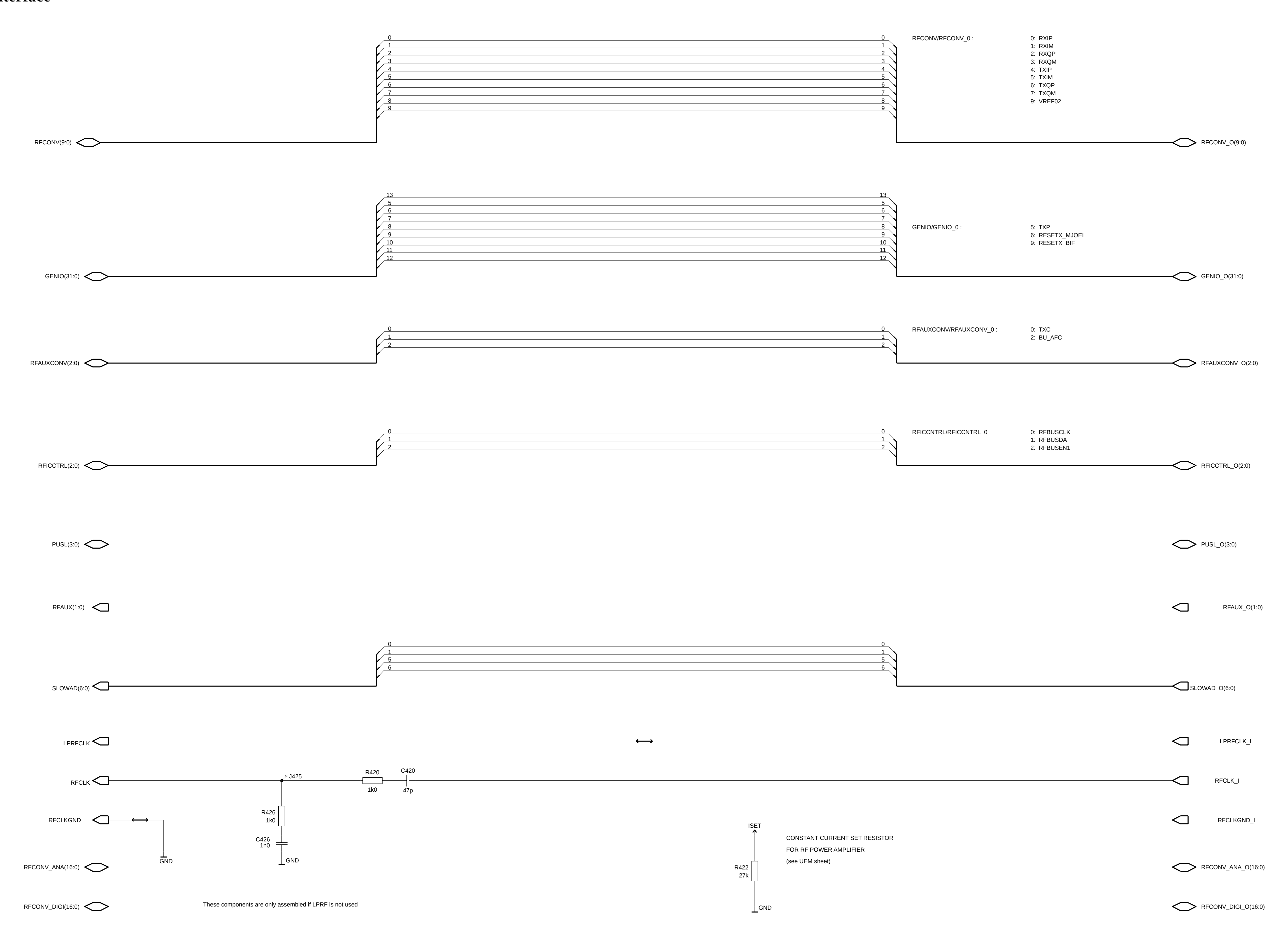


Discrete Capacitors for Memory Without VFlash





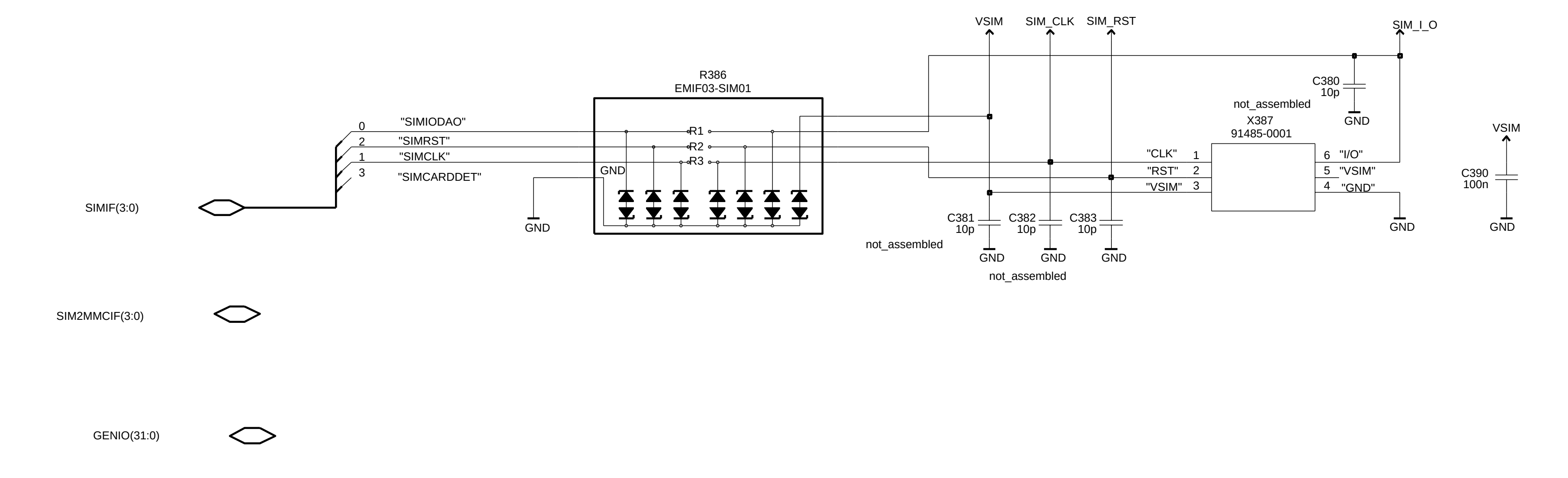
RF/BB Interface



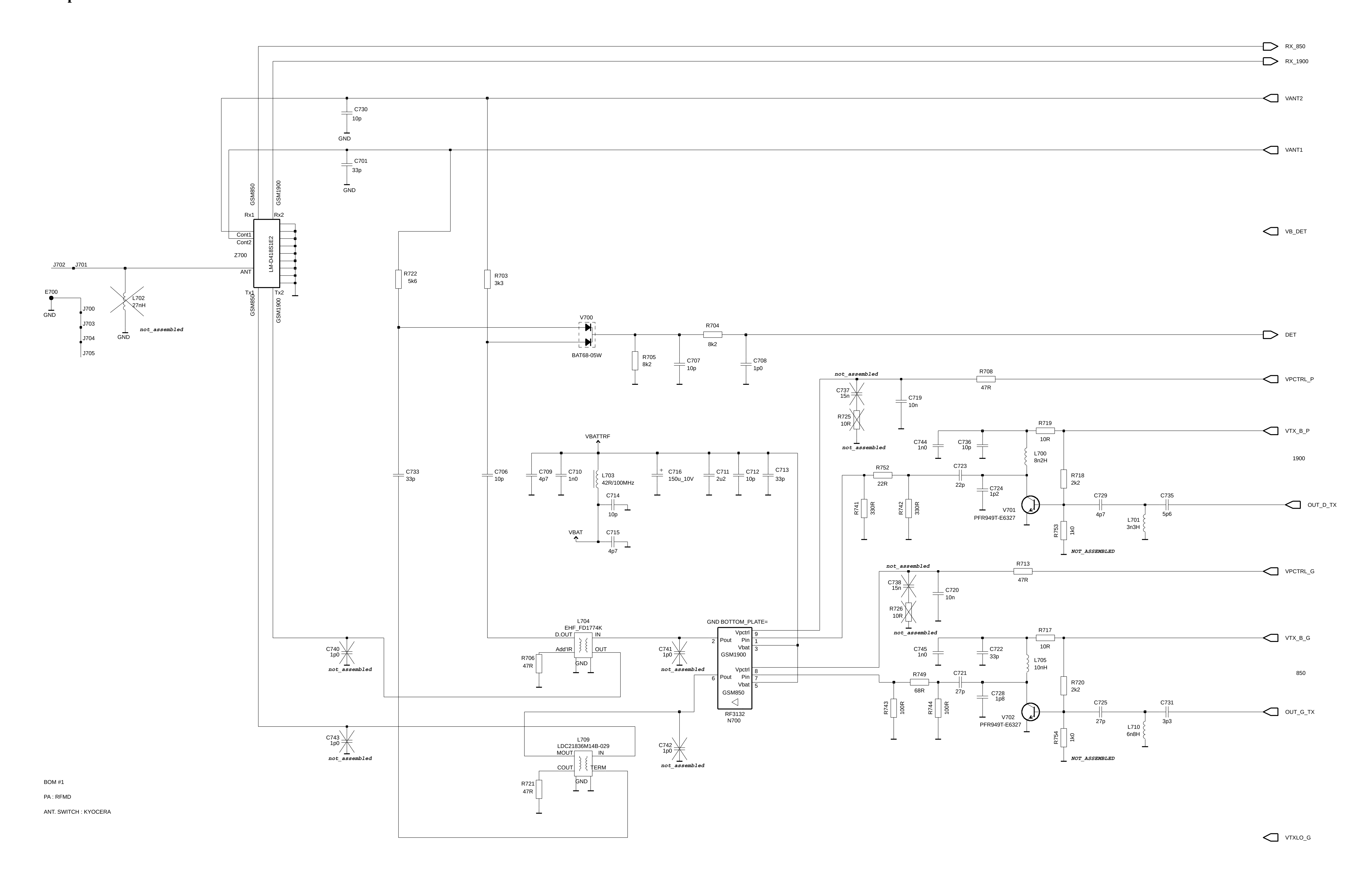
Audio Interface



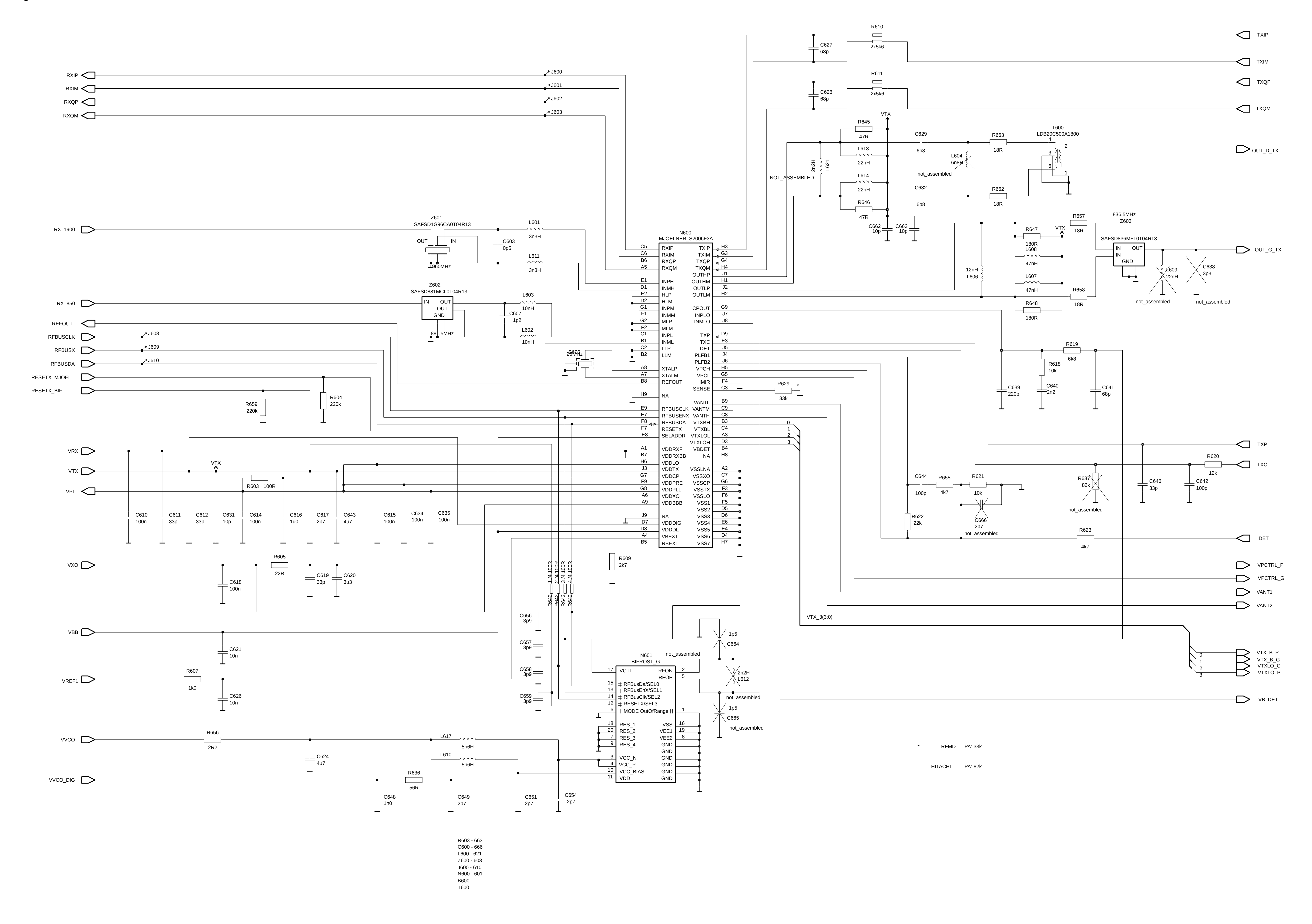
SIM Interface



Power Amplifier



Mjoelner



Component Layout - Top

